

Natural light Closest to the Sunlight

Z Power Chip on board – ZC series

SAWS0661A



Product Brief

Description

- The Sunlike COB series deliver natural sunlight spectrum.
- It is especially designed light source for human centric lighting by using violet LED and TRI-R phosphor technology.
- It's thermal management is excellent than other power LED solutions with wider Metal area.
- Sunlike series are ideal light sources for commercial lightings including shop, museum, hospital and other premium light quality required applications.



Features and Benefits

- CRI Typ. 97 (on the BBL)
- Size 13.5mm * 13.5mm
- MacAdam 3-step binning
- Uniformed Shadow
- Excellent Thermal management
- RoHs compliant

Key Applications

- Commercial Spot, Downlight
- Replacement lamps bulb, PAR, MR16
- Industrial
- Residential

Table 1. Product Selection Table

Part Number	c	ССТ		
Fait Nulliber	Color	Тур.	Тур.	
		6500K	97	
	Cool White	5000K	97	
SAWS0661A	A Neutral White 4	4000K	97	
	Warm White	3000K 97		
	vvarni vvnite	2700K	97	



Table of Contents

Inde	x	
•	Product Brief	1
•	Table of Contents	2
•	Performance Characteristics	3
•	Characteristics Graph	5
•	Color Bin Structure	14
•	Mechanical Dimensions	16
•	Packaging Specification	17
•	Product Nomenclature (Labeling Information)	19
•	Handling of Silicone Resin for LEDs	20
•	Precaution for Use	21
•	Company Information	24



C



SEOUL SEMICONDUCTOR

SAWS0661A - Chip on Board

Performance Characteristics

Table.2 Electro Optical Characteristics, T_i=85°C

Part Number	ССТ (К) ^[1]	Typical Luminous Flux ^[2] Φ _V ^[3] (Im)	Typical Forward Voltage (V _F) ^[4]	CRI ^[5] , R _a	Viewing Angle (degrees) 2O ½
	Тур.	0.17A	0.17A	Тур.	Тур.
	6500	580	36.1	97	115
	5000	585	36.1	97	115
SAWS0661A	4000	560	36.1	97	115
	3000	515	36.1	97	115
	2700	500	36.1	97	115

5000

Notes :

- (1) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram. Color coordinate : ± 0.005 , CCT $\pm 5\%$ tolerance.
- (2) Seoul Semiconductor maintains a tolerance of \pm 7% on flux and power measurements.
- (3) Φ_V is the total luminous flux output as measured with an integrating sphere.
- (4) Tolerance is $\pm 3\%$ on forward voltage measurements.
- (5) Tolerance is ± 2 on CRI measurements.

* No values are provided by real measurement. Only for reference purpose.

Performance Characteristics

SEOUL

Table.3 Absolute Maximum Ratings

Davamatar	Cumbol		Value	Unit	
Parameter	Symbol	Min.	Тур.	Max.	Unit
Forward Current	I _F	-	0.17	0.26	А
Power Dissipation	P _d	-	6.1	10.2	W
Junction Temperature	Τ _j	-	-	125	٥C
Operating Temperature	T _{opr}	- 40	-	85	°C
Surface Temperature	Τ _s	- 40	-	105	°C
Storage Temperature	T_{stg}	- 40	-	105	٥C
Thermal resistance (J to S) [1]	Rθ _{J-S}	-	1.5	-	K/W
ESD Sensitivity(HBM)		Class	3A JESD22-A	114-E	

Notes :

(1) Thermal resistance : $R\theta_{J-S}$ (Junction / solder)

At thermal resistance, J to S means junction to COB's substrate bottom.

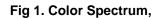
•LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.

•Thermal resistance can be increased substantially depending on the heat sink design/operating condition, and the maximum possible driving current will decrease accordingly.

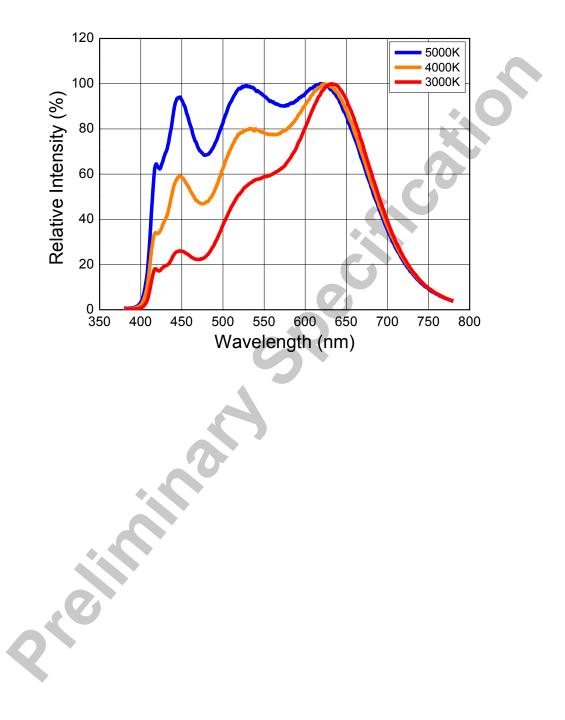
•All measurements were made under the standardized environment of Seoul Semiconductor..



Performance Characteristics

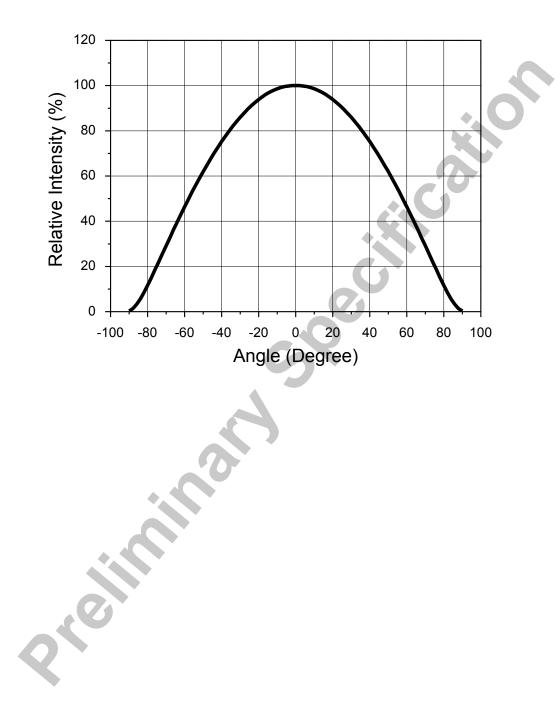


SEOUL





Performance Characteristics



SEOUL



Performance Characteristics

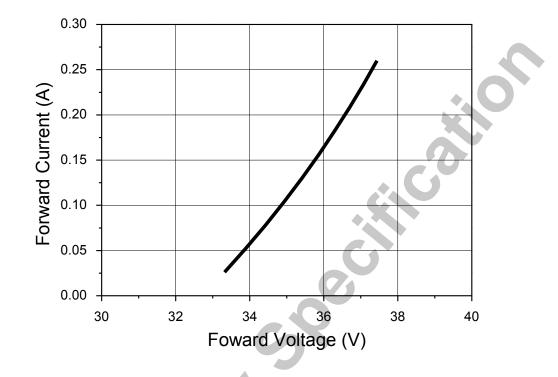
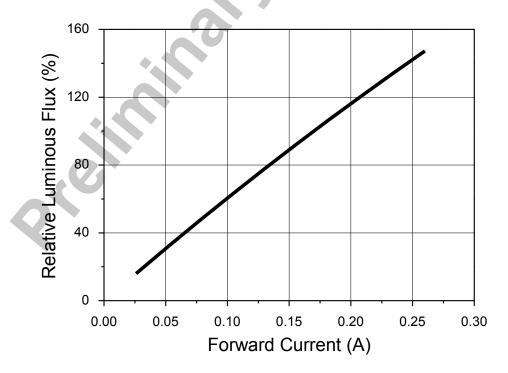


Fig 3. Forward Voltage vs. Forward Current, T_j =85°C

Fig 4. Forward Current vs. Relative Luminous Flux, Ti=85°C





Performance Characteristics

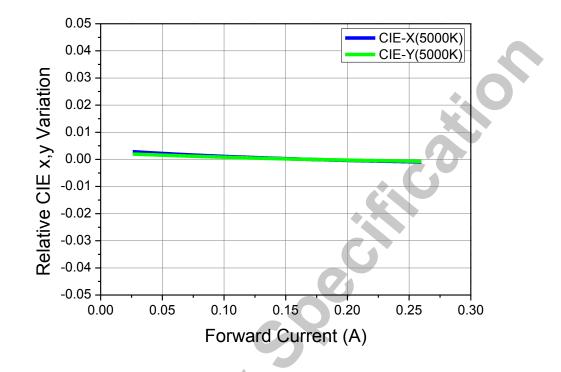
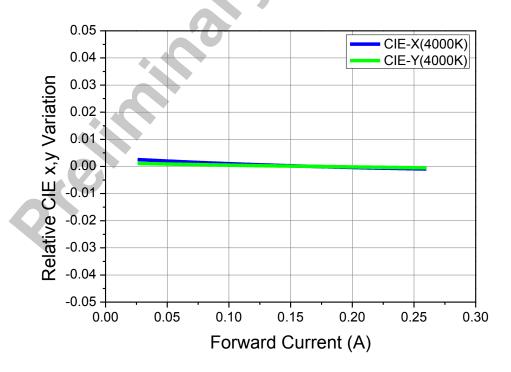


Fig 5. Forward Current vs. CIE x,y Shift, T_j=85 °C (5000K)

Fig 6. Forward Current vs. CIE x,y Shift, T_i=85°C (4000K)





Performance Characteristics

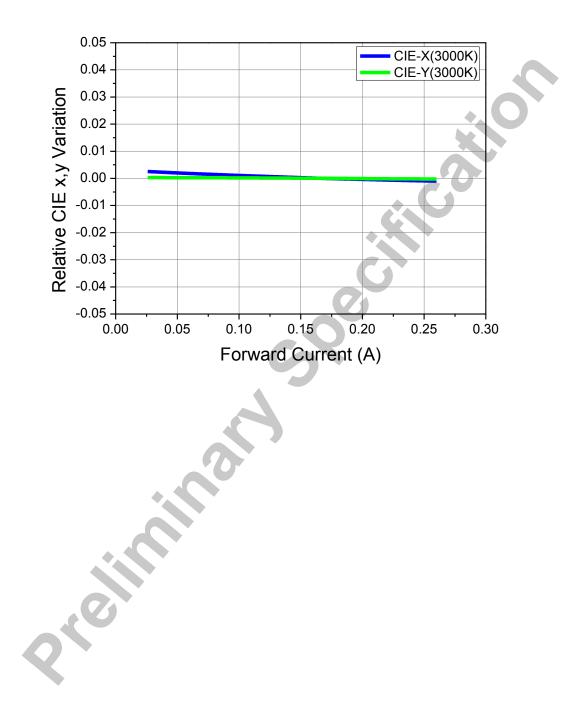


Fig 7. Forward Current vs. CIE x,y Shift, T_j=85 °C (3000K)



Performance Characteristics

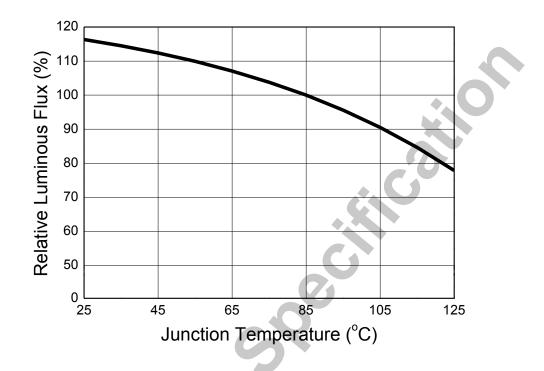
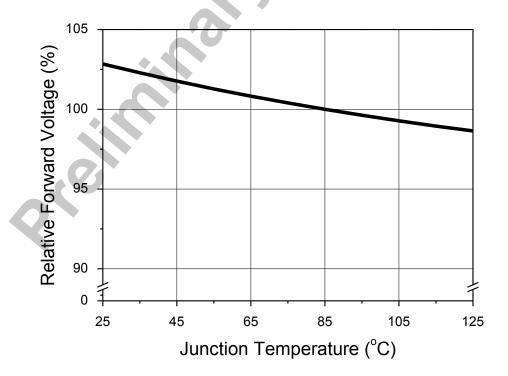


Fig 8. Junction Temperature vs. Relative Luminous Flux, I_F =0.17A

Fig 9. Junction Temperature vs. Forward Voltage, I_F=0.17A





Performance Characteristics

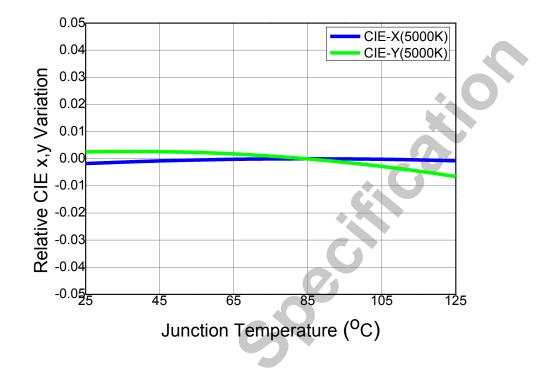
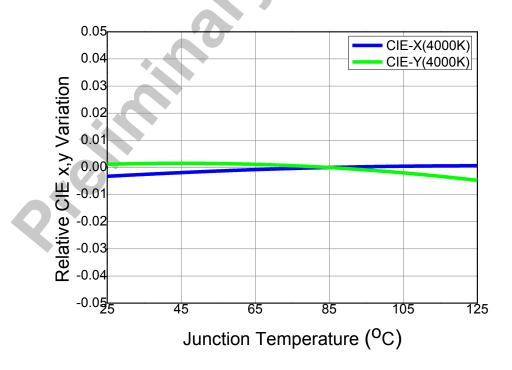


Fig 10. Junction Temperature vs. CIE x,y Shift, I_F=0.17A (5000K)

Fig 11. Junction Temperature vs. CIE x,y Shift, I_F=0.17A (4000K)





Performance Characteristics

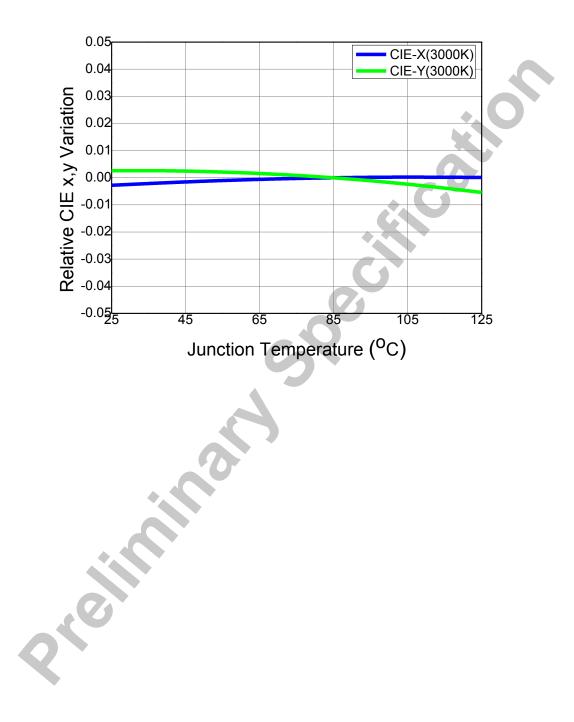
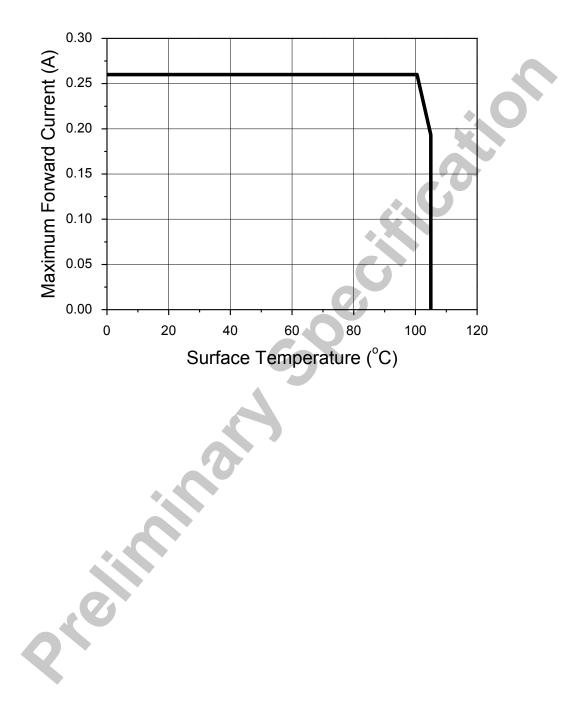


Fig 12. Junction Temperature vs. CIE x,y Shift, I_F=0.17A (3000K)



Performance Characteristics







Color Bin Structure

SEOUL

Table 4. Bin Code description, $T_j=85^{\circ}C$, $I_F=0.17A$

Part	Lui	ninous Fl (Im)	lux	Co Chrom	lor aticity		ical Forw /oltage (V)		CF	રા
Number	Bin Code	Min.	Тур.	Bin Code	Тур. ССТ	Bin Code	Min.	Max.	Bin Code	Тур.
	A6	535	580	AE3	6500K	Н	34.3	38.0	S	97
	A6	540	585	CE3	5000K	Н	34.3	38.0	S	97
SAWS0661A	A6	515	560	EE3	4000K	Н	34.3	38.0	S	97
	A6	475	515	GE3	3000K	н	34.3	38.0	S	97
	A6	460	500	HE3	2700K	Н	34.3	38.0	S	97
						S				
					9					
			0							
		\mathbf{C}								
24	2	~								



Color Bin Structure

SEOUL

CIE Chromaticity Diagram, T_i=85°C

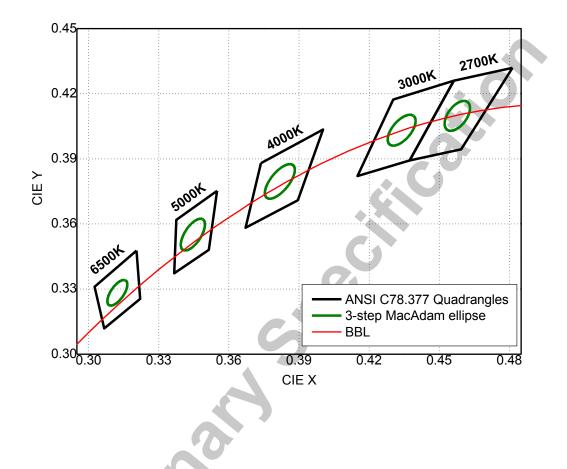


Table 5. 3-step MacAdam ellipse color bin definitions

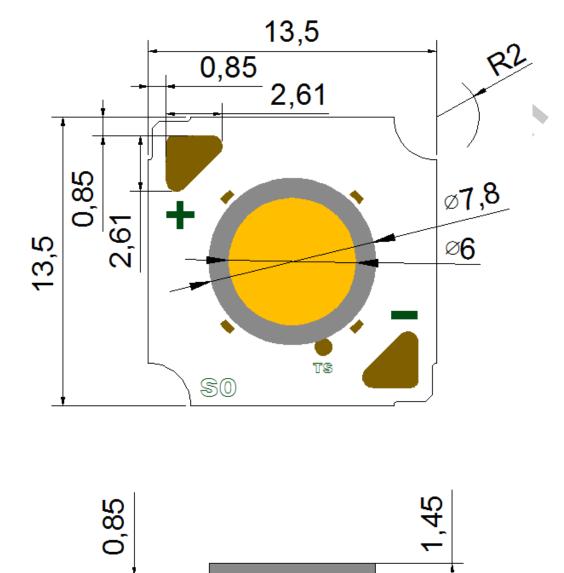
Color Region	CCT	Cente	Point	Major Axis Minor Axis		Rotation Angle
	(K) (a) (a)	(b)	(θ)			
3-step MacAdam ellipse	6500	0.3123	0.3283	0.00669	0.00285	58.38
	5000	0.3446	0.3551	0.00822	0.00354	59.62
	4000	0.3818	0.3797	0.00939	0.00402	54.00
	3000	0.4339	0.4033	0.00834	0.00408	53.17
	2700	0.4578	0.4101	0.00774	0.00411	57.28



SEOUL SEMICONDUCTOR

SAWS0661A – Chip on Board

Mechanical Dimensions



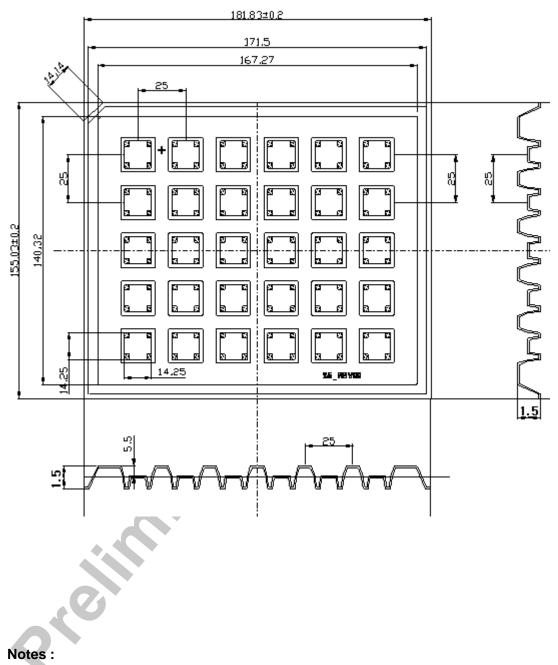


- 1. All dimensions are in millimeters.
- 2. Scale : none
- 3. Undefined tolerance is ± 0.3 mm



Packaging Specification

SEOUL



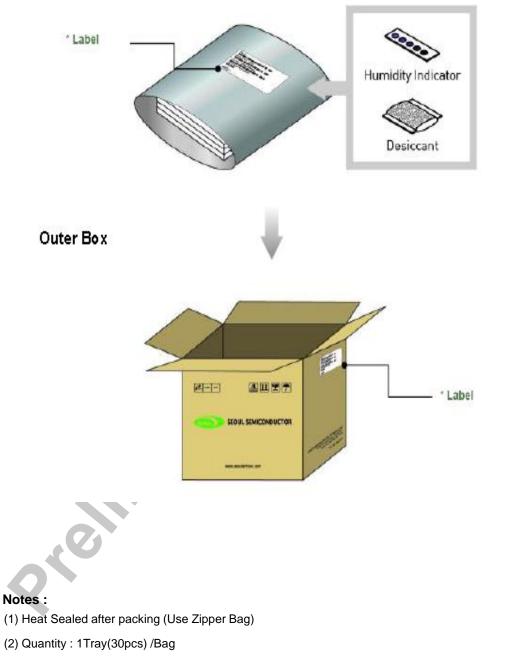
- (1) Quantity : 30pcs/Tray
- (2) All dimensions are in millimeters (tolerance : ± 0.3)
- (3) Scale none

SEOUL SE

SAWS0661A – Chip on Board

Packaging Specification

Aluminum Bag



(3) Smallest packing quantity : 3Bags(90pcs) / small box



Product Nomenclature

SEOUL

/	RANK : Z ₁ Z ₁ Z ₂ Z ₂ Z ₂ Z ₃ Z ₄	
	QUANTITY : 30	
	LOT NUMBER : $Y_{1}Y_{2}Y_{3}Y_{4}Y_{5}Y_{6}Y_{7}Y_{8}Y_{9}Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$	
	SSC PART NUMBER : X ₁ X ₂ X ₃ X ₄ X ₅ X ₆ X ₇ X ₈ X ₉	
	SEOUL	

Table 6. Part Numbering System : X₁X₂X₃X₄X₅X₆X₇X₈X₉

Part Number Code	Description	Part Number	Value
X ₁	Company S		
X ₂	Package series A		
X ₃ X ₄	Color Specification WS		Sunlike
X ₅ X ₆	LES size 06		
X ₇	Chip Array	6	Series
X ₈	Chip Array 1		Parallel
X ₉	X ₉ Revision number A		

Table 7. Lot Numbering System : $Y_1Y_2Y_3Y_4Y_5Y_6Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$

Lot Number Code	Description
Y ₁ Y ₂ Y ₃ Y ₄ Y ₅	Date of box packing
Y ₆ Y ₇ Y ₈ Y ₉ Y ₁₀	Date of label order
Y ₁₁ Y ₁₂ Y ₁₃ Y ₁₄ Y ₁₅ Y ₁₆ Y ₁₇	Item code



SEOUL

SAWS0661A – Chip on Board

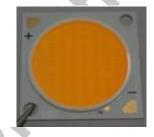
Handling of Silicone Resin for LEDs

 During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.





(3) Silicone differs from materials conventionally used for the manufacturing of LEDs.

These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust. As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of wire.

- (4) Seoul Semiconductor suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (5) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this product with acid or sulfur material in sealed space.
- (6) Avoid leaving fingerprints on silicone resin parts.



Precaution for Use

(1) Storage

To avoid the moisture penetration, we recommend storing LEDs in a dry box with a desiccant . The recommended storage temperature range is 5° C to 30° C and a maximum humidity of RH50%.

(2) For manual soldering

Seoul Semiconductor recommends the soldering condition

- (ZC series product is not adaptable to reflow process)
- a. Use lead-free soldering
- b. Soldering should be implemented using a soldering equipment at temperature lower than 350°C.
- c. Before proceeding the next step, product temperature must be stabilized at room temperature

(3) Radioactive exposure is not considered for the products listed here in.

(4) Gallium arsenide is used in some of the products listed in this publication. These products are dangerous if they are burned or shredded in the process of disposal. It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.

(5) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.

(6) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.

(7) LEDs must be stored in a clean environment. We recommend LEDs store in nitrogen-filled container.

(8) The appearance and specifications of the product may be modified for improvement without notice.

(9 Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.

(10) Attaching LEDs, do not use adhesive that outgas organic vapor.

(11) The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

(12) Please do not touch any of the circuit board, components or terminals with bare hands or metal while circuit is electrically active.

SEOUL SEMICONDUCTOR

SAWS0661A – Chip on Board

Precaution for Use

SEOUI

(13) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures ca n penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. T he result can be a significant loss of light output from the fixture. Knowledge of the properties of the m aterials selected to be used in the construction of fixtures can help prevent these issues.

(14) LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.

a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)



Precaution for Use

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package

(If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)

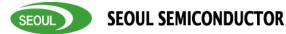
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package
- (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires.
- This damage usually appears due to the thermal stress produced during the EOS event.

c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:

- A surge protection circuit
- An appropriately rated over voltage protection device
- A current limiting device



Company Information

Published by

Seoul Semiconductor © 2013 All Rights Reserved.

Company Information

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

Legal Disclaimer

Information in this document is provided in connection with Seoul Semiconductor products. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Seoul Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party. The appearance and specifications of the product can be changed to improve the quality and/or performance without notice.